



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi Hashimoto, et al.**

Group Art Unit: **2814**

Serial No.: **10/083,533**

Examiner: **Le, Thao X.**

Filed: **February 27, 2002**

P.T.O. Confirmation No.: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A  
SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD CHARACTERISTIC  
(As amended)**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: October 17, 2003

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated June 19, 2003 for 1 month from September 19, 2003 to October 19, 2003.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

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MNL/eg  
Atty. Docket No. **020244**  
Suite 700  
1250 Connecticut Ave, N.W.  
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(202) 822-1100

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PATENT TRADEMARK OFFICE

TECHNOLOGY CENTER 2800

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